

CLAIMS

1. An integrated apparatus comprising:

a high-level-processor; and

a low-level-processor;

wherein said integrated apparatus is adapted to provide a first apparatus operated with said high-level-processor in a first mode of operation, and wherein said integrated apparatus is adapted to provide a second apparatus operated with said low-level-processor in a second mode of operation.

2. An integrated apparatus as claimed in claim 1, said low-level-processor being adapted to be powered in said second mode of operation to provide said second apparatus, while said high-level-processor remains at least substantially unpowered in said second mode of operation.

3. An integrated apparatus as claimed in claim 1, said integrated apparatus being adapted to allow at least one of: powering into said first mode of operation during times exclusive of powering into said second mode of operation, and powering into said second mode of operation during times exclusive of powering into said first mode of operation.

4. An integrated apparatus as claimed in claim 1, said low-level-processor being adapted to operate in a first manner in the first apparatus mode, and adapted to operate in a differing manner in the second apparatus mode.

5. An integrated apparatus as claimed in claim 1, said low-level-processor being provided as part of a chipset providing other components of said integrated apparatus.

6. An integrated apparatus as claimed in claim 1, wherein said first apparatus is a portable computer.

7. An integrated apparatus comprising:

high-level-processor means for providing high-level-processing; and

low-level-processor means for providing low-level-processing;

wherein said integrated apparatus is adapted to provide a first apparatus operated with said high-level-processor means in a first mode of operation, and wherein said integrated apparatus is adapted to provide a second apparatus operated with said low-level-processor means in a second mode of operation.

8. An integrated apparatus as claimed in claim 7, said low-level-processor means being adapted to be powered in said second mode of operation to provide said second apparatus, while said high-level-processor means remains at least substantially unpowered in said second mode of operation.

9. An integrated apparatus as claimed in claim 7, said integrated apparatus being adapted to allow at least one of: powering into said first mode of operation during times exclusive of powering into said second mode of operation, and powering into said second mode of operation during times exclusive of powering into said first mode of operation.

10. An integrated apparatus as claimed in claim 7, said low-level-processor means being adapted to operate in a first manner in the first apparatus mode, and adapted to operate in a differing manner in the second apparatus mode.

11. An integrated apparatus as claimed in claim 7, said low-level-processor means being provided as part of a chipset providing other components of said integrated apparatus.

12. An integrated apparatus as claimed in claim 7, wherein said first apparatus is a portable computer.

13. A method of providing an integrated apparatus comprising:
providing a high-level-processor adapted to provide high-level-processing; and
providing a low-level-processor adapted to provide low-level-processing;
providing a first apparatus by operation of said high-level-processor in a first mode of operation; and

providing a second apparatus by operation of said low-level-processor in a second mode of operation.

14. A method as claimed in claim 13, comprising:
powering said low-level-processor in said second mode of operation to provide said second apparatus, while at least substantially non-powering said high-level-processor in said second mode of operation.

15. A method as claimed in claim 13, comprising at least one of: powering into said first mode of operation during times exclusive of powering into said second mode of operation, and powering into said second mode of operation during times exclusive of powering into said first mode of operation.

16. A method as claimed in claim 13, comprising:

operating said low-level-processor in a first manner in the first apparatus mode; and
operating said low-level-processor in a differing manner in the second apparatus mode.

17. A method as claimed in claim 13, comprising providing said low-level-processor as part of a chipset providing other components of said integrated apparatus.

18. A method as claimed in claim 13, comprising providing said first apparatus as a portable computer.

19. A portable computer system adapted to be selectable into a differing apparatus, comprising:

a high-level-processor for providing high-level-processing; and

a low-level-processor for providing low-level-processing;

wherein said portable computer system is adapted to provide a portable computer apparatus operated with said high-level-processor in a portable computer mode of operation, and wherein said portable computer system is adapted to provide said differing apparatus operated with said low-level-processor in another mode of operation.

20. A portable computer system as claimed in claim 19, said portable computer system being adapted to allow at least one of: powering into said portable computer mode of operation during times exclusive of powering into said another mode of operation, and powering into said another mode of operation during times exclusive of powering into said portable computer mode of operation.